

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application Serial No. .... 10816772  
Filing Date ..... April 1, 2004  
Confirmation No. ..... 2790  
Inventor..... Ramaswamy, D. V. Nirmal  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 1792  
Examiner ..... Robert M. Kunemund  
Attorney's Docket No. ..... MI22-2520  
Title:.....Methods of Depositing an Elemental Silicon-Comprising Material  
Over a Substrate

## **SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References - - See attached Form PTO/SB/08a

The attached Form PTO/SB/08a is submitted in compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, and your attention is directed to the references listed on the attached Form PTO/SB/08a. No admission is made regarding whether the submitted references are prior art.

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: February 20, 2008 \_\_\_\_\_ By: Jennifer J. Taylor/  
Jennifer J. Taylor, Ph.D.  
Reg. No. 48711